

Micron SSDs: A secure foundation for your data¹

IT managers, chief information officers (CIOs) and chief information security officers (CISOs) face ever-increasing threats from attackers attempting to illicitly acquire sensitive and valuable data. These threats call for a layered approach to data protection that addresses active data, as well as stored data.

Micron® SSDs help provide a secure defense for the base layer of your data systems. Data-at-rest protection covers data stored at various locations throughout the enterprise — from the notebook to the data center and in the cloud. Micron delivers SSDs for all these applications, built with advanced security technology, to help shield data from loss and to protect the security and integrity of the SSD and its firmware.

Threats to data at rest

Self-encrypting drives (SEDs) are widely recommended as the foundation to provide advanced protection against some of the most prevalent and dangerous threats to data at rest, including:

- **Lost or stolen computers or storage devices:** When powered off or in hibernation mode, SEDs automatically lock, inhibiting access to all data stored on the drive and requiring a passcode entry before being unlocked, decrypted and used.² Extremely robust 256-bit encryption means that the data is essentially unreadable without proper credentials.³
- **Sophisticated HDD/SSD attacks:** Sophisticated hackers have devised ways to attack HDDs and SSDs at their most basic level — firmware. Micron SSDs, whether encrypted or not, include advanced protection features to ensure the authenticity of the firmware. Micron SSDs allow firmware updates in the field while significantly reducing the risk of loading a corrupted or counterfeited firmware image.⁴

Benefits of Micron self-encrypting drives (SEDs)

Encryption that does not slow you down

Built-in encryption engines perform at full interface speed, without using CPU cycles. Encrypted SSDs transfer data at the same speed as their unencrypted counterparts.⁵

Broad range of security options

Micron designs our SSDs with robust encryption and authentication features, as well as industry-standard data sanitization methods. Micron's encrypted SSDs meet multiple industry standards for security. Some Micron NVMe™ SSDs are also available with Micron's Secure Execution Environment (SEE).⁶

Security for the entire lifecycle of the device

- **Simplified key management:** The SSD generates and securely stores the encryption keys, removing that function from the host computer or data center.
- **Fast and secure device retirement/redeployment:** The cryptographic erase function securely sanitizes all user data in seconds, eliminating the need for costly and slow sanitation methods and enabling redeployment instead of wasteful device destruction.

1. No hardware, software or system can provide absolute security under all conditions. Micron assumes no liability for lost, stolen or corrupted data arising from the use of any Micron products, including those products that incorporate any of the mentioned security features.
2. SED behavior noted by on [this page of the Storage Networking Industry Association \(SNIA\) website](#).
3. Estimate only, actual value may vary. [This article on ssldragon.com offers a real-world example of the difficulty of breaking 256-bit encryption](#).
4. One example of a firmware attack is noted on [this page of the Kaspersky.com website](#) (this is just an example).
5. Comparisons based on Micron testing with standard benchmarks on SED and non-SED SSDs (same model number and advertised capacity).
6. Statement based on SSD product briefs available from [the SSD page on the micron.com website](#); the SEE is a dedicated security processing hardware with physical isolation for security-related function isolation built into specific SSD controller.

Micron SSD portfolio security features⁷

Table 1 shows security-related features and functions of Micron client and data center SSDs. When the feature or function is supported, a checkmark appears in the corresponding cell.

| Feature / Micron SSD | Micron Client SSDs | | | | | Micron Data Center SSDs | | | | | |
|---|--------------------|------|------|------|------|-------------------------|----------|------|------|------|-----|
| | 2500 | 2550 | 2650 | 3500 | 5400 | 6500 ION | 6550 ION | 7450 | 7500 | 9550 | XTR |
| Self-encrypted drive (SED) SKUs | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Cryptographic erase | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Sanitize | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Secure erase | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| NAND block erase | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Signed firmware | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| TCG Enterprise | | | | | ✓ | | | | | | |
| TCG Opal | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| TCG Pyrite | ✓ | ✓ | ✓ | ✓ | | | | | | | |
| TAA-compliant SKUs | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Micron Secure Execution Environment (SEE) | | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Security Protocol and Data Model (SPDM) | | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Ability to debug SSD without exposing user data | | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| FIPS certifiable | | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Attestation (SPDM version) | | | | | | 1.2 | 1.2 | 1.1 | 1.2 | 1.2 | 1.2 |
| Secure boot | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Security engine: AES 256 | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Security engine: RSA 4096 | ✓ | ✓ | ✓ | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Security engines: SHA-384, SHA-512 | | | | | | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |

Table 1: Security-related features

Feature-rich Micron self-encrypting SSDs⁷

Micron secure firmware helps protect the storage platform against low-level attacks. Features like Advanced Encryption Standard (AES) 256-bit hardware encryption and standards-based security features work together to help protect your data (Micron is a contributing member of the [Trusted Computing Group](#)).⁸

| Feature | Description |
|----------------------------------|---|
| Self-encrypted drive (SED) | Self-encrypting drive; an SSD with an internal encryption mechanism or mechanisms. |
| Cryptographic erase | The process of erasing an SED by permanently destroying the encryption key. |
| Sanitize | A process by which data is removed from the storage device to a point that exceeds the ability to reconstruct the data by known forensic means. |
| Secure erase | Executing a block erase on each element in the NAND flash array in the SSD. |
| NAND block erase | The process of erasing an SSD via the NAND block erase command. |
| Signed firmware | Authenticates SSD firmware prior to updating it, which helps protect our SSDs against malicious firmware. |
| TCG Enterprise, Opal, and Pyrite | Trusted Computing Group standards (see the Trusted Computing Group website for additional details on each standard). Different standards may be used for different SSDs in different deployments. TCG Enterprise: A storage management system where management rules are defined by a central, trusted computer; TCG Opal: Defines the basic features required for a storage device security is professional and business use; TCG Pyrite: Defines a storage device security system that is simple to set up and use. |

Table 2: Security feature details

7. Additional details on security features are available in [this document on the micron.com website](#).

8. A list of contributing members is available on this page on [the trustedcomputing.org website](#).

| Feature | Description |
|--|---|
| TAA-compliant options ⁹ | A standard providing assurance that Micron SSDs designated as TAA compliant are manufactured in TAA-designated countries to ease supply chain management for government users. |
| Micron Secure Execution Environment (SEE) | A dedicated security processing unit in select Micron SSD controllers. The SEE consists of a dedicated ROM, firmware, and security microprocessor. The secure microprocessor is electrically isolated from other microprocessors within the SSD controller; SEE execution cannot be preempted by non-secure code. This isolation significantly reduces the opportunity for the security functionality of the storage device to be accidentally or maliciously circumvented. |
| Security Protocol and Data Model (SPDM) | A standard that defines messages, data objects, and sequences for performing message exchanges between devices over a variety of transport and physical media. See The SPDM Protocol: Overview of Component Integrity as a Security Standard for additional details |
| Tamper-evident seals | Seals or labels encapsulating material or that provide evidence tampering (on SSDs with cases) Tamper evidence for caseless SSDs is the package itself. |
| Ability to debug SSD without exposing user data | The ability to troubleshoot SSD issues without having access to the user data on that SSD. |
| FIPS certifiable at the ASIC level | The SSD controller (the ASIC) is certifiable. This ensures that the SSD controller can meet the Federal Information Processing Standards (FIPS) for security. A list of certified devices is available on the Cryptographic Module Validation Program (CMVP) on the csrc.nist.gov website. |
| Attestation | A secure mechanism to validate trust in server components such as SSDs. |
| Secure boot | Utilizes a trust relationship between different entities where each entity honors the other's authenticity, and each step is subject to attestation prior to execution (such as during power on). Micron SSD secure boot utilizes a chain of trust mechanism in which the SSD firmware bootloader trusts the immutable SSD ROM, and the main firmware in turn trusts the bootloader. |
| Security engine: AES 256 | An open, standard encryption mechanism utilizing a 256-bit verification. |
| Security engine: RSA 4096 | A 4096-bit public key encryption mechanism based on an algorithm publicly disclosed in 1977. |
| Security engine: SHA-512 | A one-way secure hash function that generates a 512-bit message digest. |
| Attestation (Security Protocol and Data Model, SPDM) | Attestation is the mechanism through which state and integrity are verified. SPDM is a standard for secure communication protocols and data models to protect data integrity and confidentiality. The SPDM standard is managed by DMTF . |
| Hardware root of trust | A method that helps ensure that the initial code executed during the boot process is from a trusted source and cannot be tampered with (defined in the Open Compute Project (OCP) Datacenter NVMe SSD Specification version 2.5). |
| Ability to debug SSD without exposing user data | A feature that helps enable support teams are able to troubleshoot and resolve SSD issues without exposing user data stored on the SSD. |
| FIPS certifiable and the ASIC level | The SSD controller (the ASIC) is certifiable. This ensures that the SSD controller can meet the Federal Information Processing Standards (FIPS) for security. A list of certified devices is available on the Cryptographic Module Validation Program (CMVP) on the csrc.nist.gov website. |
| Secure boot | A method to help ensure that only trusted firmware is loaded during the boot process, enhancing the overall security of the SSD |
| AES-256 | The NIST-selected cryptographic algorithm submitted by two Belgian cryptologists, Vincent Rijmen and Joan Daemen. (The algorithm's common name, Rijndael, is derived from combining their surnames). |
| RSA 4096 | An RSA encryption key that is 4096 bits in length. |
| SHA-512 | A 512-bit secure hash algorithm. |
| Cryptographic hardware-based root of trust | Cryptographic keys are anchored to the hardware. |
| DICE | Device Identify Composition Engine is a hardware-based security engine designed to enhance device-level security. For more information on DICE, see this page on the trustedcomputinggroup.org website. |

Table 2: Security feature details (continued)

9. TAA-compliant devices available; contact your Micron sales team for additional information. Statement based on SSD product briefs available on [the SSD page on \[micron.com\]\(#\)](#).

micron.com/ssd

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